

Form PTO-1449		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. M122-2374	SERIAL NO. Filed Herewith		
LIST OF ART CITED BY APPLICANT (Use several sheets if necessary)				APPLICANT H. Montgomery Manning			
				FILING DATE Filed Herewith	GROUP 2826		
U.S. PATENT DOCUMENTS							
*Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate	
ON	AA	5,552,628	09/1996	Watanabe			
	AB	5,585,664	12/1996	Ito			
	AC	5,583,357	12/1996	Choe			
	AD	4,418,239	11/1983	Larson			
	AE	4,693,530	09/1987	Stillie			
	AF	4,781,620	11/1988	Tengler			
	AG	4,933,743	06/1990	Thomas			
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	AI	5,117,276	05/1992	Thomas			
	AJ	5,123,325	06/1992	Turner			
	AK	5,176,538	01/1993	Hansell, III			
ON	AL	4,686,759	08/1987	Pals			
FOREIGN PATENT DOCUMENTS							
	Document Number	Date	Country	Class	Subclass	Translation	
						Yes	No
ON	AM	JP 0097300	04/1994	Japan			
ON	AN	JP 054925	02/1990	Japan			
	AO						
	AP						
	AQ						
OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)							
UN	AR	Thomas, Michael E., "VLSI Multilevel Micro-Coaxial Interconnects for High Speed Devices", 1990, IEEE, pps. 55-58.					
	AS						
	AT						
EXAMINER ON				DATE CONSIDERED 11/22/04			
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ON	AA	4,922,323	05/1990	Potter	[Wavy line]		
	AB	5,323,048	06/1994	Onuma			
	AC	5,329,155	07/1994	Lao			
	AD	5,471,093	11/1995	Cheung			
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	AJ	5,519,239	05/1996	Chu			
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	AL	5,956,615	09/1999	Nguyen et al			

FOREIGN PATENT DOCUMENTS							
	Document Number	Date	Country	Class	Subclass	Translation	
						Yes	No
	AM						
	AN						
	AO						
	AP						
	AQ						

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)			
	AR		
	AS		
	AT		

EXAMINER <div style="text-align: center;">ON</div>	DATE CONSIDERED <div style="text-align: center;">11/22/04</div>
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